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PTO/SB/21 (07-06)

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TRANSMITTAL FORM (to be used for all correspondence after initial filing)	Application Number	10/786,250-Conf. #2714
	Filing Date	February 25, 2004
	First Named Inventor	Richard Schubert
	Art Unit	2188
	Examiner Name	C. E. Walter
Total Number of Pages in This Submission	Attorney Docket Number	A0312.70518US00

ENCLOSURES (Check all that apply)		
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<div>Remarks</div>		

SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT			
Firm Name	WOLF, GREENFIELD & SACKS, P.C.		
Signature			
Printed name	Edmund J. Walsh		
Date	November 3, 2006	Reg. No.	32,950

Certificate of Mailing Under 37 CFR 1.8(a)	
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Dated: 11/3/06	Signature: (Elyse B. Pino)



Docket No.: A0312.70518US00
(PATENT)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Richard Schubert
Serial No.: 10/786,250
Confirmation No.: 2714
Filed: February 25, 2004
For: CACHE MEMORY WITH IMPROVED REPLACEMENT POLICY
Examiner: WALTER, Craig E.
Art Unit: 2188

Certificate of Mailing Under 37 CFR 1.8(a)

I hereby certify that this paper (along with any paper referred to as being attached or enclosed) is being deposited with the U.S. Postal Service on the date shown below with sufficient postage as First Class Mail, in an envelope addressed to: Mail Stop Amendment, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

Dated: 11/3/06

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RESPONSE TO RESTRICTION REQUIREMENT

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

In response to the Restriction Requirement dated October 3, 2006, Applicants elect Group I, with traverse.

The present application describes a data cache with a replacement policy based on priority of data stored in the cache. Thirty claims have been presented, of which only three claims are independent. All of the claims relate to an aspect of such a cache.

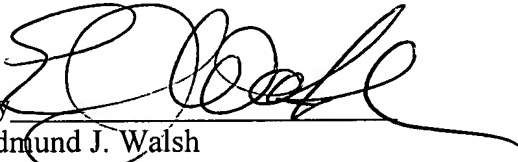
Nonetheless, the Examiner has issued a three-way restriction requirement, with Group I relating to "a method of operating a cache in a digital computer system," Group II relating to "a processor system having a cache," and Group III relating to a "processor system, comprising...at least one cache memory."

Claims in all three groups relate to the same general subject matter. Further, claims in all three groups recite a limitation involving "priority." For example, claim 1 recites: "associating the priority of the new item with the selected memory location in the cache." Claim 19 recites: "a first field, indicating a relative priority of the item stored in the associated location in the data array," and claim 26 recites "at least a second control bit for each memory location indicating a priority of information stored in the memory location."

Therefore, the claims are sufficiently related that they can and should be examined together. Applicant respectfully requests that the restriction requirement be withdrawn and all of the claims be examined on the merits.

Respectfully submitted,

Dated: November 3, 2006


By
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